

CYPRESS SEMICONDUCTOR INVENTION DISCLOSURE FORM

1. INVENTOR(S)

DISCLOSURE NO CD50060

A. Name Paul Cheng Initials IXL Empl. No. Ext. No.

Citizenship USA Dept # Home Phone No.

Home Mailing Address _____

B. Name Kuang-Yu Chen Initials ICK Empl. No. Ext. No.

Citizenship USA Dept # Home Phone No.

Home Mailing Address _____

C. Name Frank Hwang Initials IHY Empl. No. Ext. No.

Citizenship _____ Dept # _____ Home Phone No. _____

Home Mailing Address _____

D. Name Eric Chen Initials ICH Empl. No. Ext. No.

Citizenship Taiwan Dept # Home Phone No.

Home Mailing Address _____

E. Name Hyunbae Kim Initials HYK Empl. No. Ext. No.

Citizenship Korea Dept # Home Phone No.

Home Mailing Address _____

2. TITLE OF INVENTION Method and Apparatus for Overclocking Recovery in a PLL

3. CONCEPTION OF INVENTION

A. Date of first drawing or drawings _____
Where can first drawing be found _____

B. Date of first written description _____
Where is description found _____

Inventor(s) _____ Date _____

Inventor(s) _____ Date _____

Inventor(s) _____ Date _____

Witnessed, Read, and Understood by: _____ Date _____

Witnessed, Read, and Understood by: _____ Date _____

(Each page upon which information is entered should be signed and witnessed.)

CYPRESS SEMICONDUCTOR INVENTION DISCLOSURE FORM

C. Date of first oral disclosure to others _____

To whom? _____

4. CONSTRUCTION OF DEVICE

A. Date Completed _____ not completed yet – being simulated (to be completed) _____

B. Was prototype made? _____

C. By whom made? _____

D. Where can the prototype be found? _____

5. TEST OF DEVICE

A. Date: _____ Witness(es): _____

B. Results: _____

6. SALE

A. Was invention sold or offered for sale? Yes___ No_X__

B. Was invention used to make, assemble or test a commercial product? Yes___ No_X__

C. Will invention be sold, offered for sale, sampled, or used to make, assemble or test a commercial product? Yes_X__ No___

D. Actual or estimated date of first sale, offer or commercial use _____

E. Is invention part of a product for which there is a data sheet? Yes_X__ No___ (If yes, attach a copy) (datasheet still in construction)

F. Actual or estimated date of publication, release or availability of data sheet _____

7. USE

A. Is invention presently being used? Yes___ No_X__

B. Are there specific plans for its use in near future? In what products or processes?

8. RELATED PRINTED PUBLICATIONS, PATENTS, PATENT APPLICATIONS

9. WAS INVENTION Conceived (Yes___ (No_X__ Constructed (Yes___ (No_X__ Tested (Yes___ (No_X__ during performance of Government Contract?

Contract Number _____

Inventor(s) _____ Date _____

Inventor(s) _____ Date _____

Inventor(s) _____ Date _____

Witnessed, Read, and Understood by: _____ Date _____

Witnessed, Read, and Understood by: _____ Date _____

(Each page upon which information is entered should be signed and witnessed.)

CYPRESS SEMICONDUCTOR INVENTION DISCLOSURE FORM

(Give Full Contract Number)

=====

The description of invention should be written in the inventor's own words and generally should follow the outline given below. Sketches, prints, photos, and other illustrations, as well as memos or reports of any nature in which the invention is referred to, if available, should form a part of this disclosure and reference can be made thereto in the descriptions of the invention's construction and operation.

Inventor(s) _____ Date _____

Inventor(s) _____ Date _____

Inventor(s) _____ Date _____

Witnessed, Read, and Understood by: _____ Date _____

Witnessed, Read, and Understood by: _____ Date _____

(Each page upon which information is entered should be signed and witnessed.)

CYPRESS SEMICONDUCTOR INVENTION DISCLOSURE FORM

FOR ANSWERS TO THE FOLLOWING QUESTIONS, USE THE REMAINDER OF SHEET AND THE ATTACHED SHEET(S).

1. General purpose of invention. State in general terms the objects of the invention.
2. Describe old technology, if any, for performing the function of the invention. Provide references, if available.
3. Indicate the disadvantages of the old technology.
4. Describe your invention and its construction, showing the changes, additions and improvements over the old method.
5. Give details of its operation (i.e., how is your invention used?), if not already described under 4.
6. State the advantages of your invention over what has been done before.
7. Indicate any alternate component(s) and/or method(s) of construction.
8. If a joint invention, indicate what contribution was made by each inventor.
9. Describe the features that are believed to be new.
10. State opinion of relative value of invention.
11. After the disclosure is prepared, it should be signed by the inventor(s) and then read and signed by two witnesses in the space provided at the bottom of each sheet.

=====

1. General purpose: The purpose of the invention is to provide for a recovery from a system hang due to over-clocking.
2. Old technology: Many system (e.g. motherboard) manufacturers use Cypress programmable clock chips to over-clock their systems. Sometimes the over-clocking causes the microprocessor and/or the system to hang. The system may hang if the clock speed is set too high. The microprocessor may hang if there is an abrupt change in clock frequency, causing a glitch. To provide for a recovery mechanism, some motherboard manufacturers have incorporated a watchdog timer or RC delay circuitry on the motherboard itself.

Inventor(s) _____ Date _____

Inventor(s) _____ Date _____

Inventor(s) _____ Date _____

Witnessed, Read, and Understood by: _____ Date _____

Witnessed, Read, and Understood by: _____ Date _____

(Each page upon which information is entered should be signed and witnessed.)

CYPRESS SEMICONDUCTOR INVENTION DISCLOSURE FORM

3. Invention/Operation/Advantages: The invention is an on-board watchdog timer and reset generation circuitry to provide for a recovery mechanism when the system or microprocessor hangs. The BIOS program requests a clock frequency change via the IIC interface on the clock chip. When the new frequency is loaded in to the IIC interface, the clock chip generates a reset output signal. This reset signal may be used by the system manufacturer to reset components that require a reset once the frequency is changed. The watchdog timer is also started when the new frequency is loaded in to the IIC interface.

The watchdog timer counts to a certain value (the value may be user-programmable through the IIC interface) and if the system has not come back when the timer reaches its maximum count, another reset output signal is generated. This reset signal may be used to reset the entire system.

The clock circuit also includes a mechanism to allow the frequency to drop down to a recovery frequency when the watchdog timer times out. This recovery frequency is programmable through the IIC interface.

4. Value of Invention: This invention is believed to be valuable because it is included in the [REDACTED] product.

Inventor(s) _____ Date _____

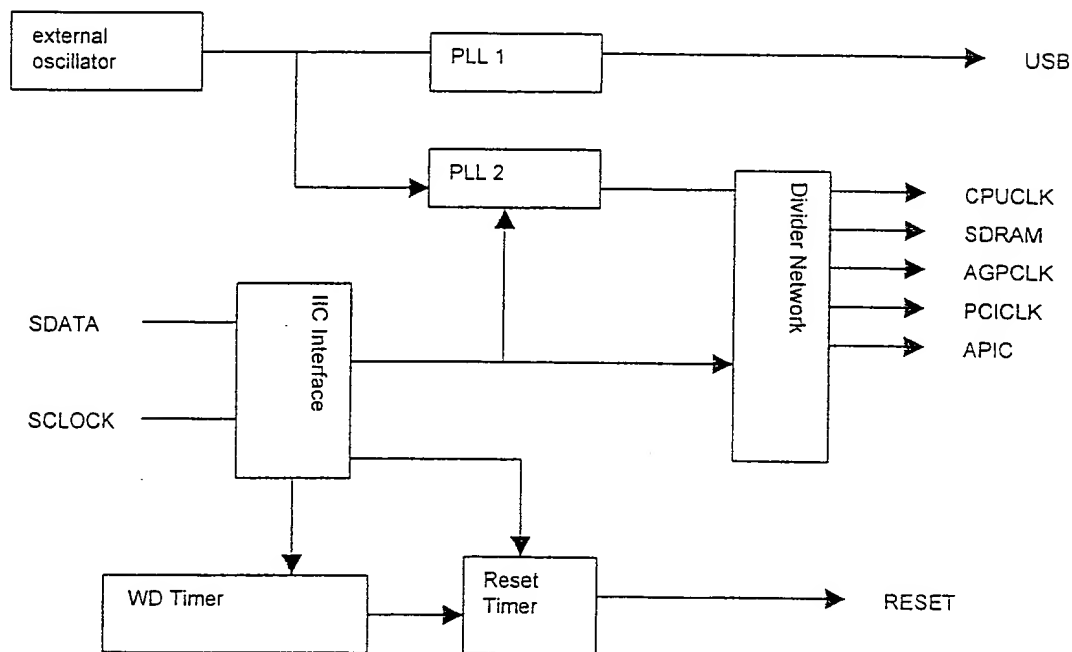
Inventor(s) _____ Date _____

Inventor(s) _____ Date _____

Witnessed, Read, and Understood by: _____ Date _____

Witnessed, Read, and Understood by: _____ Date _____

(Each page upon which information is entered should be signed and witnessed.)

CYPRESS SEMICONDUCTOR INVENTION DISCLOSURE FORM

Inventor(s) _____ Date _____

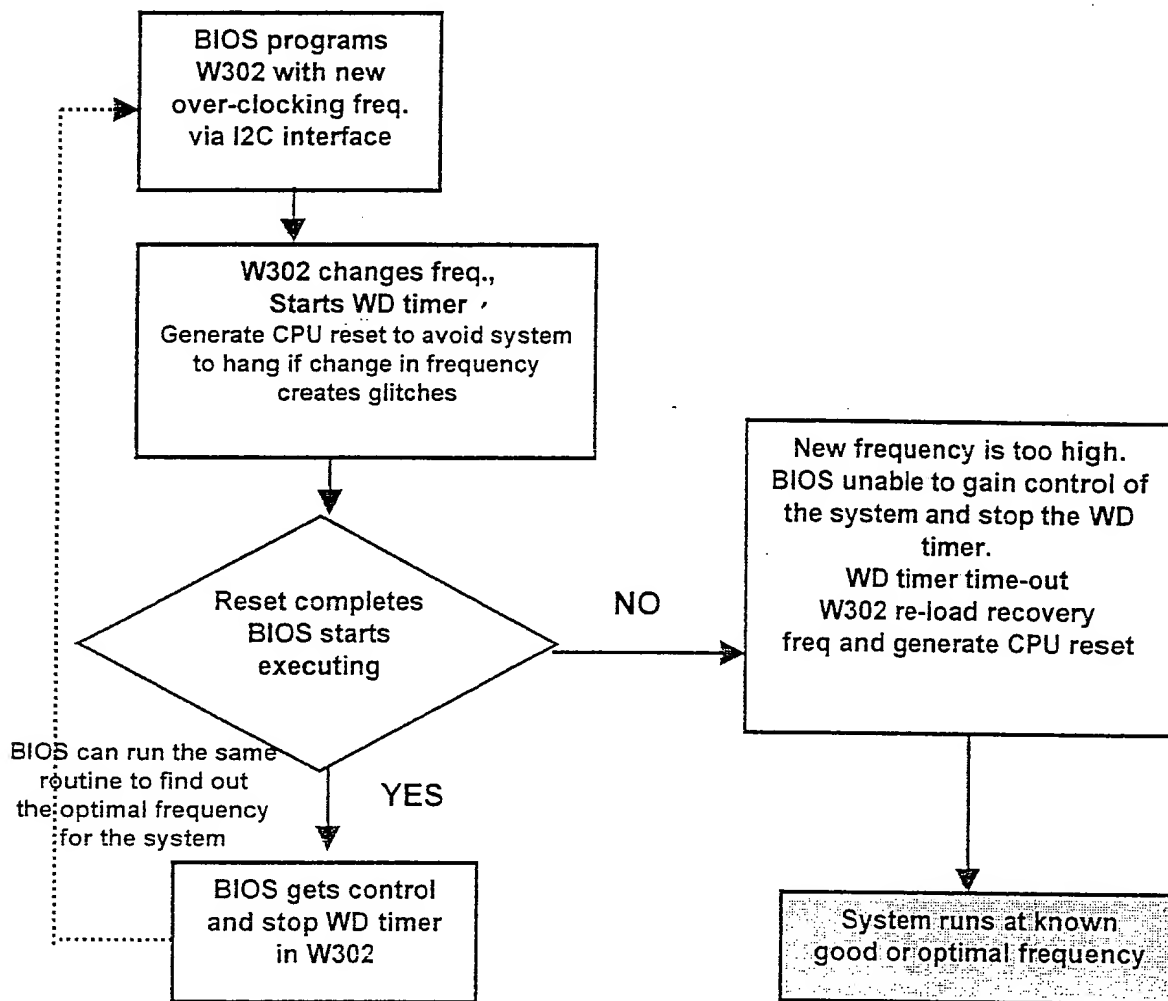
Inventor(s) _____ Date _____

Inventor(s) _____ Date _____

Witnessed, Read, and Understood by: _____ Date _____

Witnessed, Read, and Understood by: _____ Date _____

(Each page upon which information is entered should be signed and witnessed.)

CYPRESS SEMICONDUCTOR INVENTION DISCLOSURE FORM

Inventor(s) _____ Date _____

Inventor(s) _____ Date _____

Inventor(s) _____ Date _____

Witnessed, Read, and Understood by: _____ Date _____

Witnessed, Read, and Understood by: _____ Date _____

(Each page upon which information is entered should be signed and witnessed.)